

## AMENDMENTS TO THE SPECIFICATION

Please add the following two new paragraphs after the paragraph beginning on page 8, line 10:

Figure 3 is a flowchart illustrating a process of automatically detecting exceptions in a sequence of pipelined instructions in accordance with an embodiment of present invention.

Figure 4 is a flowchart illustrating a process of automatically detecting floating point exceptions in accordance with an embodiment of present invention.

Please add the following two new paragraphs prior to the paragraph beginning on page 25, line 10:

Figure 3 is a flowchart illustrating a process 300 of automatically detecting exceptions in a sequence of pipelined instructions in accordance with an embodiment of present invention. In step 310, a command that tests for generated floating point status exceptions is automatically inserting into a sequence of instructions to be executed. In step 320, a test by the command that indicates a generated floating point status exception during pipelined execution of the sequence of instructions is responded to by returning execution to an instruction in the sequence of instructions at which correct state is known. In step 330, each instruction in the sequence is executed singly to completion until a floating point exception is detected.

Figure 4 is a flowchart illustrating a process 400 of automatically detecting floating point exceptions in a sequence of host instructions translated from a sequence of target instructions, in accordance with an embodiment of present invention. In step 410, a command that detects report of a floating point exception is automatically inserted in a sequence of pipelined host instructions translated from a sequence of target instructions. In

step 420, the command detects a report of a floating point instruction exception during execution of the sequence of pipelined host instructions. In step 430, host instructions translated from the sequence of target instructions are executed singly to completion in response to the command detecting the report in step 420. The executing singly starts at a point in the sequence of target instructions at which correct state is known. In step 440, a floating point instruction exception is detected during the executing of the host instructions singly.